## **IN THE CLAIMS:**

Please amend the claims as follows:

- 1. (ONCE AMENDED) An adapter comprising:
  - (a) a first and a second surface;
  - (b) at least one first interconnect on the first surface,
- (c) at least one second interconnect on the second surface, the at least one second interconnect comprising a through hole connect;
  - (d) at least one connective path between the first and second interconnects, and
  - (e) a signal modifying circuit between the first interconnect and the second interconnect.
- 2. (ONCE AMENDED) The adapter of Claim 1 wherein the <u>at least one</u> first [set of] interconnect[s are] <u>is</u> physically spaced to correspond to a first pin configuration of a power module.
- 3. (ONCE AMENDED) The adapter of Claim 1 wherein the <u>at least one</u> second [set of] interconnect[s are] <u>is</u> physically spaced to correspond to a second pin configuration of an end user's circuit board.
- 4. The adapter of Claim 1 wherein a signal modifying circuit acts upon an input to the adapter.
- 5. The adapter of Claim 1 wherein a signal modifying circuit acts upon an output to the adapter.
- 6. The adapter of Claim 2 wherein the power module is a DC-to-DC converter.
- 7. (ONCE AMENDED) The adapter of Claim 2 wherein the power module is an AC-to-DC [inverter] converter.
- 8. The adapter of Claim 2 wherein the power module is a DC-to-AC inverter.
- 9. The adapter of Claim 1 wherein the first interconnects comprise surface mount connects.
- 10. The adapter of Claim 1 wherein the first interconnects comprise through hole connects.
- 11. (Cancel) The adapter of Claim 1 wherein the second interconnects comprise surface mount connects.
- 12. (Cancel) The adapter of Claim 1 wherein the second interconnects comprise through hole connects.

- 13. (ONCE AMENDED) The adapter of Claim 1 wherein the [second interconnects] signal modifying circuit comprise a filter.
- 14. (ONCE AMENDED) The adapter of Claim 1 wherein the [second interconnects] signal modifying circuit comprise an overvoltage protection device.

A clean copy of all pending claims is as follows:

- 1. (ONCE AMENDED) An adapter comprising:
  - (a) a first and a second surface;
  - (b) at least one first interconnect on the first surface,
- (c) at least one second interconnect on the second surface, the at least one second interconnect comprising a through hole connect;
  - (d) at least one connective path between the first and second interconnects, and
  - (e) a signal modifying circuit between the first interconnect and the second interconnect.
- 2. (ONCE AMENDED) The adapter of Claim 1 wherein the at least one first interconnect is physically spaced to correspond to a first pin configuration of a power module.
- 3. (ONCE AMENDED) The adapter of Claim 1 wherein the at least one second interconnect is physically spaced to correspond to a second pin configuration of an end user's circuit board.
- 4. The adapter of Claim 1 wherein a signal modifying circuit acts upon an input to the adapter.
- 5. The adapter of Claim 1 wherein a signal modifying circuit acts upon an output to the adapter.
- 6. The adapter of Claim 2 wherein the power module is a DC-to-DC converter.



- 7. (ONCE AMENDED) The adapter of Claim 2 wherein the power module is an AC-to-DC converter.
- 8. The adapter of Claim 2 wherein the power module is a DC-to-AC inverter.
- 9. The adapter of Claim 1 wherein the first interconnects comprise surface mount connects.
- 10. The adapter of Claim 1 wherein the first interconnects comprise through hole connects.
- 11. (Cancel)
- 12. (Cancel)
- 13. (ONCE AMENDED) The adapter of Claim 1 wherein the signal modifying circuit comprise a filter.





14. (ONCE AMENDED) The adapter of Claim 1 wherein the signal modifying circuit comprise an overvoltage protection device.